Taiwan Semiconductor

1A, 50V - 600V Ultra Fast Surface Mount Rectifier

FEATURES

- AEC-Q101 qualified
- Glass passivated chip junction
- Ideal for automated placement
- Ultra Fast recovery time for high efficiency
- Low forward voltage, low power loss
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

APPLICATIONS

- DC to DC converter
- Automotive application
- Car lighting
- Snubber
- Freewheeling application

MECHANICAL DATA

- Case: DO-214AA (SMB)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.090g (approximately)

KEY PARAMETERS				
PARAMETER	VALUE	UNIT		
I _F	1	А		
V _{RRM}	50 - 600	V		
I _{FSM}	40, 35	А		
T _{J MAX}	175	°C		
Package	DO-214AA (SMB)			
Configuration	Single die			

Pb-Free ROHS COMPLIANT FREE



DO-214AA (SMB)



ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise noted)								
		MUR	MUR	MUR	MUR	MUR	MUR	
PARAMETER	SYMBOL	105S	110S	115S	120S	140S	160S	UNIT
		Н	Н	Н	Н	Н	н	
Marking code on the device		MUR	MUR	MUR	MUR	MUR	MUR	
Marking bode on the device		105S	110S	115S	120S	140S	160S	
Repetitive peak reverse voltage	V _{RRM}	50	100	150	200	400	600	V
Reverse voltage, total rms value	V _{R(RMS)}	35	70	105	140	280	420	V
Forward current	I _F				1			А
Surge peak forward current, 8.3ms single half sine-wave superimposed on rated load	I _{FSM}		4	0		3	5	A
Junction temperature	TJ			- 55 to	o +175			°C
Storage temperature	T _{STG}			- 55 to	o +175			°C



THERMAL PERFORMANCE			
PARAMETER	SYMBOL	ТҮР	UNIT
Junction-to-lead thermal resistance	R _{ejl}	17	°C/W

ELECTRICAL SPEC		TA = 25°C unless othe	erwise noted)			
PARAMETER		CONDITIONS	SYMBOL	ТҮР	MAX	UNIT
	MUR105SH MUR110SH MUR115SH MUR120SH	I _F = 1A, T _J = 25°C	V _F	-	0.875	V V V V
Forward values (1)	MUR140SH MUR160SH			-	1.250	V V
	MUR105SH MUR110SH MUR115SH MUR120SH	I _F = 1A, T _J = 150°C		-	0.710	V V V V
	MUR140SH MUR160SH			-	1.050	V V
MUR110SH MUR110SH MUR120SH MUR140SH MUR160SH @ rated V _R ⁽²⁾ MUR105SH MUR105SH MUR105SH MUR105SH MUR105SH MUR110SH MUR110SH MUR120SH MUR120SH	MUR105SH MUR110SH MUR115SH MUR120SH	T _J = 25°C	. I _R	-	2	μΑ μΑ μΑ μΑ
	MUR140SH MUR160SH			-	5	μA μA
	MUR105SH MUR110SH MUR115SH MUR120SH	T _J = 150°C		-	50	μΑ μΑ μΑ μΑ
	MUR140SH MUR160SH			-	150	μA μA
Reverse recovery time	MUR105SH MUR110SH MUR115SH MUR120SH	$I_F = 0.5A, I_R = 1.0A$ $I_{rr} = 0.25A$	t _{rr}	-	25	ns ns ns ns
	MUR140SH MUR160SH			-	50	ns ns

Notes:

1. Pulse test with PW = 0.3ms

2. Pulse test with PW = 30ms



ORDERING INFORMATION ORDERING CODE⁽¹⁾ PACKAGE PACKING MUR1xSH DO-214AA (SMB) 3,000 / Tape & Reel

Notes:

1. "x" defines voltage from 50V(MUR105SH) to 600V(MUR160SH)



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CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

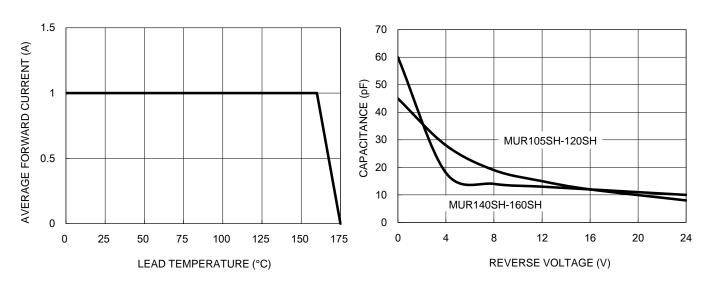
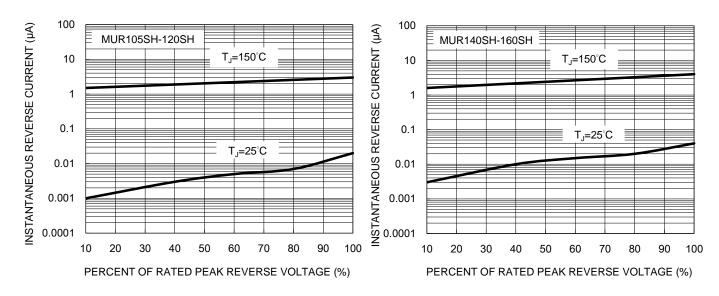


Fig.1 Forward Current Derating Curve

Fig.2 Typical Junction Capacitance

Fig.3 Typical Reverse Characteristics

Fig.4 Typical Reverse Characteristics

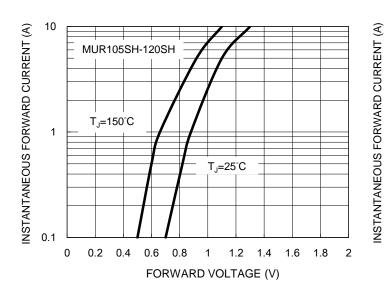




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CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)



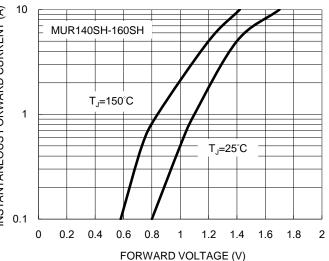
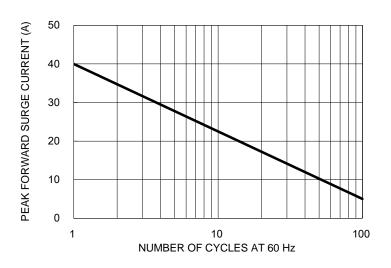


Fig.5 Typical Forward Characteristics Fig.6 Typical Forward Characteristics

Fig.7 Maximum Non-repetitive Forward Surge Current



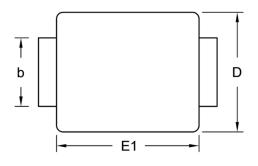


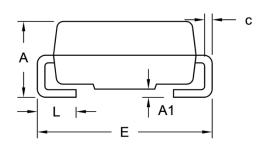
PACKAGE OUTLINE DIMENSIONS

TAIWAN SEMICONDUCTOR

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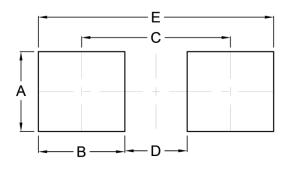
DO-214AA (SMB)





DIM.	Unit (mm)		Unit	(inch)	
	Min.	Max.	Min.	Max.	
A	1.95	2.65	0.077	0.104	
A1	0.05	0.20	0.002	0.008	
b	1.95	2.20	0.077	0.087	
с	0.15	0.31	0.006	0.012	
D	3.30	3.95	0.130	0.156	
E	5.10	5.60	0.201	0.220	
E1	4.05	4.60	0.159	0.181	
L	0.75	1.60	0.030	0.063	

SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
A	2.30	0.091
В	2.50	0.098
С	4.30	0.169
D	1.80	0.071
E	6.80	0.268

MARKING DIAGRAM



P/N	= Marking Code
G	= Green Compound

YW = Date Code

F = Factory Code



MUR105SH - MUR160SH

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